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CONFIRMATION NO. 8465

SERIAL NUMBER 10/534,049	FILING OR 371(c) DATE 05/06/2005 RULE	CLASS 365	GROUP ART UNIT 2827	ATTORNEY DOCKET NO. TSUT.0087
APPLICANTS Tomonori Sekiguchi, Tama, JAPAN; Shinichi Myatake, Ome, JAPAN; Takeshi Sakata, Hino, JAPAN; Riichiro Takemura, Tokyo, JAPAN; Hiromasa Noda, Tokyo, JAPAN; Kazuhiko Kajigaya, Iruma, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP02/11659 11/08/2002				
** FOREIGN APPLICATIONS *****				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY JAPAN	SHEETS DRAWING 23	TOTAL CLAIMS 21
Verified and Acknowledged Examiner's Signature _____ Initials _____		INDEPENDENT CLAIMS 5		
ADDRESS 38327				
TITLE Semiconductor memory				
FILING FEE RECEIVED 1350	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	